EAST Search History

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	41093	retention adj time	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:02
L2	2350	refresh adj period	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:02
L3	768	refresh adj frequency	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:03
L4	3008	2 or 3	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:03
L5	180	1 and 4	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:03
L6	64187	tagged or tagging	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:06
L7	1	5 and 6	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:07
L8	39	harrand-michel.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:07
L9	5	bulone-joseph.in.	US-PGPUB; USPAT; USOCR; EPO; JPO	OR	ON	2006/11/08 15:07

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = HARRAND
First Name = MICHEL

Application#	Patent#	Status	Date Filed	Title	Inventor Name
07557718	5126734	150	07/26/1990	SWITCHING MATRIX CROSSPOINT	HARRAND, MICHEL
07926815	5265041	150	08/07/1992	BIDIMENSIONAL FIR FILTER	HARRAND, MICHEL
07931827	Not Issued	166	08/18/1992	IMAGE PREDICTOR	HARRAND, MICHEL
08111624	5349547	150	08/25/1993	BIDIMENSIONAL FIR FILTER	HARRAND, MICHEL
08240144	5592428	150	05/10/1994	DYNAMIC MEMORY	HARRAND, MICHEL
08269326	RE35483	150	06/30/1994	SWITCHING MATRIX CROSSPOINT	HARRAND, MICHEL
08385074	6236683	150	02/07/1995	IMAGE PREDICTOR	HARRAND, MICHEL
08465810	5590070	150	06/06/1995	DYNAMIC MEMORY	HARRAND, MICHEL
08525468	5995513	150	09/08/1995	MULTITASK PROCESSING SYSTEM	HARRAND, MICHEL
08990925	6144321	150	12/15/1997	MICROPROCESSOR DEDICATED TO PROCESSING OF BIT STREAMS IN A SYSTEM OF COMPRESSION/DECOMPRESSION OF ANIMATED IMAGES	HARRAND, MICHEL
08991022	6133859	150	12/15/1997	MICROPROCESSOR DEDICATED TO PROCESSING OF BIT STREAMS IN A SYSTEM OF COMPRESSION/DECOMPRESSION OF ANIMATED IMAGES	HARRAND, MICHEL
08994953	5987488	150	12/19/1997	MATRIX PROCESSOR	HARRAND, MICHEL
09047142	Not	161	03/24/1998	CODER/DECODER OF ANIMATED	HARRAND,

	Issued			IMAGES WITH A CACHE MEMORY	MICHEL
09276603	6215706	150	03/25/1999	FAST STRUCTURE DRAM	HARRAND, MICHEL
09420129	6914908	150	10/19/1999	MULTITASK PROCESSING SYSTEM	HARRAND, MICHEL
09715904	6675256	150	11/17/2000	FAST DRAM CONTROL METHOD AND ADAPTED CONTROLLER	HARRAND, MICHEL
09730498	6631441	150	12/04/2000	DRAM READ AND WRITE CIRCUIT	HARRAND, MICHEL
09769009	Not Issued	160	01/24/2001	Fast structure dram	HARRAND, MICHEL
09827298	Not Issued	160	04/05/2001	Image predictor	HARRAND, MICHEL
09915761	6944748	150	07/26/2001	Signal processor executing variable size instructions using parallel memory banks that do not include any no-operation type codes, and corresponding method	HARRAND, MICHEL
09927085	Not Issued	161	08/09/2001	Fast structure DRAM	HARRAND, MICHEL
10055510	Not Issued	160	01/23/2002	Fast structure DRAM	HARRAND, MICHEL
10075001	Not Issued	161	02/13/2002	Fast random access DRAM management method	HARRAND, MICHEL
10668060	Not Issued	41	09/22/2003	Fast random access DRAM management method	HARRAND, MICHEL
10766291	Not Issued	83	01/26/2004	Process for refreshing a dynamic random access memory and corresponding device	HARRAND, MICHEL
11018864	Not Issued	30	12/21/2004	Dynamic random access memory having at least two buffer registers and method for controlling such a memory	HARRAND, MICHEL
11301635	Not Issued	25	12/13/2005	Memory circuit, such as a DRAM, comprising an error correcting mechanism	HARRAND, MICHEL
11346796	Not Issued	30	02/03/2006	Dynamic memory for a cellular terminal	HARRAND, MICHEL

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	**First Name**	
harrand	michel	Search

To go back use Back button on your browser toolbar.

PALM INTRANET

Inventor Name Search Result

Your Search was:

Last Name = BULONE
First Name = JOSEPH

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08662778	6138264	150	06/10/1996	SYNDROME CALCULATION CIRCUIT	BULONE, JOSEPH
10075001	Not Issued	161	02/13/2002	Fast random access DRAM management method	BULONE, JOSEPH
10668060	Not Issued	41	09/22/2003	Fast random access DRAM management method	BULONE, JOSEPH
10766291	Not Issued	83	01/26/2004	Process for refreshing a dynamic random access memory and corresponding device	BULONE, JOSEPH

Inventor Search Completed: No Records to Display.

Search Another: Inventor

Last Name	First Name	
bulone	joseph	Search

To go back use Back button on your browser toolbar.

Back to PALM | ASSIGNMENT | OASIS | Home page